

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DFKTB-A-D
PRODUCT NAME: 11/34 MEMORY MANAGEMENT ACCESS KEYS TEST
DATE: DECEMBER 21, 1975
MAINTAINER: DIAGNOSTIC GROUP
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1.0 ABSTRACT

THIS PROGRAM CHECKS THE OPERATION OF EACH ACCESS KEY FOR EACH OF THE FOUR UNIBUS CYCLES (OR COMBINATION OF CYCLES) WHICH MAY REFERENCE AN ADDRESS THRU SEGMENTATION. THESE CYCLES ARE DATI, DATO (NO DATIP), DATIP-DATO, AND DATIP-DATOB. EACH OF THESE CASES IS TESTED WITH AND WITHOUT MEMORY MANAGEMENT ENABLE SET. THUS EIGHT CASES ARE TESTED FOR EACH KEY. SR0, SR1, SR2, THE CORRESPONDING PDR'S, AND THE PROPER EXECUTION OR PREVENTION OF EXECUTION OF THE INSTRUCTION ARE CHECKED IN EACH CASE.

2.0 REQUIREMENTS

2.1 EQUIPMENT

PDP 11/34

2.2 STORAGE

THE PROGRAM REQUIRES 5K OF MEMORY, STARTING AT LOCATION 0.

3.0 LOADING PROCEDURE

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

4.0 STARTING PROCEDURE

4.1 NORMAL DIAGNOSTIC OPERATION

SET DESIRED SWITCH REGISTER SETTINGS (ALL DOWN FOR WORST CASE).
(USE SOFTWARE SWITCH REG. AT LOC. 176 IF NECESSARY)
START AT 200
THE PROGRAM WILL RING THE BELL ON COMPLETION OF A PASS.

4.2 SINGLE SUBTEST LOOP (TESTX)

LOAD THE ADDRESS OF THE DESIRED SUBTEST
(THE ADDRESS OF THE TESTXX TAG) INTO THE LOCATION "RETRNX"
SET THE OPERATIONAL SWITCH SETTINGS DESIRED
(SW11 MUST BE SET TO ZERO).
START AT 210.

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100 5.0 OPERATING PROCEDURE
101
102 5.1 OPERATIONAL SWITCH SETTINGS
103
104 @NOTE: IF NO HARDWARE SWITCH REGISTER IS AVAILABLE, THE PROGRAM
105 WILL AUTOMATICALLY USE THE CONTENTS OF LOC. 176 AS THE
106 SOFTWARE SWITCH REGISTER.
107 THE USER SHOULD SET THIS LOCATION BEFORE STARTING
108 THE PROGRAM.
109
110 BIT15=1 -- HALT ON ERROR
111 BIT14=1 -- SCOPE LOOP
112 BIT13=1 -- INHIBIT PRINTOUT
113 BIT11=1 -- INHIBIT ITERATIONS
114 BIT10=1 -- HALT AT END OF CURRENT TEST
115 NEXT TEST NUMBER IN R0
116
117 5.2 SUBROUTINE ABSTRACTS
118
119 5.2.1 SCOPE
120
121 THIS SUBROUTINE CALL IS PLACED BETWEEN EACH SUBTEST. IT RECORDS
122 THE STARTING ADDRESS OF EACH SUB-TEST AS IT IS BEING ENTERED.
123 IF A SCOPE LOOP IS REQUESTED, IT WILL JUMP TO THE START OF THE
124 SUBTEST THAT THE SCOPE LOOP IS REQUESTED FOR. IF SCOPE LOOP IS
125 NOT REQUESTED, THERE WILL BE 1024 ITERATIONS ON THAT SUBTEST
126 BEFORE THE NEXT SUBTEST IS ENTERED. SWITCH 11 ON A 1 INHIBITS
127 ITERATION OF SUBTESTS.
128
129 5.2.2 HLT
130
131 THIS EMT CALLS THE SUBROUTINE PRINT, WHICH PRINTS OUT THE LOCATION
132 COUNTER AT THE TIME OF FAILURE AND THE CONTENTS OF THE PROCESSOR STATUS
133 REGISTER. NOTE THAT THE LOCATION COUNTER WILL BE THE ADDRESS
134 OF THE HLT PLUS TWO.
135
136 5.2.3 TRAPCATCHER
137
138 THIS IS A SERIES OF INSTRUCTIONS STARTING AT LOCATION 0 DESIGNED
139 TO DETECT AND ISOLATE UNEXPECTED TRAPS AND INTERRUPTS TO THE
140 TRAP AND INTERRUPT VECTOR AREA OF MEMORY.
141
142 IF A HALT OCCURS IN THE TRAP OR INTERRUPT AREA, EXAMINE REGISTER
143 SIX. IT WILL CONTAIN THE CURRENT STACK ADDRESS. THE CONTENTS
144 OF THE CURRENT STACK ADDRESS IS THE VALUE OF THE LOCATION COUNTER
145 WHEN THE TRAP OR INTERRUPT OCCURRED.
146
147 5.2.4 TESTX (SINGLE SUBTEST LOOP)
148
149 THIS ROUTINE ALLOWS A SINGLE SUBTEST TO BE RUN CONTINUOUSLY FOR

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SCOPE LOOP PURPOSES. WHILE A SCOPE LOOP SWITCH OPTION EXISTS,
IT REQUIRES THAT YOU ARE WITHIN THE TEST IN WHICH YOU WISH TO
LOOP. IN SOME CASES (SUCH AS WITH INTERMITTENT FAILURES) THAT'S
NOT EASY TO DO. THIS SUBROUTINE ALLOWS YOU TO LOAD THE ADDRESS
OF ANY SUBTEST AND THEN GO DIRECTLY TO THAT TEST.

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158
159 5.2.5 EMTSRV (EMT DECODER)
160
161 THIS ROUTINE DECODES ALL EMT CALLS, INCLUDING PATCHES AND THE
162 HLT CALL WHICH PASSES CONTROL TO THE PRINT ROUTINE.
163
164 5.2.6 CLRALL
165
166 THIS ROUTINE CLEARS ALL THE PAR'S AND PDR'S OF THE MEMORY MANAG.,
167 AS WELL AS SR0.
168
169 5.2.7 RWALL
170
171 THIS ROUTINE MAPS ALL PAGES TO BANK 0 BY CLEARING ALL THE PAR'S.
172 ALL PAGES ARE MADE 4K READ-WRITE BY LOADING ALL THE PDR'S WITH
173 THE VALUE 77406.
174
175 5.2.8 SETUP
176
177 THIS ROUTINE FIRST CALLS RWALL TO MAP ALL THE PAGES 4K, RW, BANK 0.
178 IT THEN SETS THE KEY FOR KERNEL PAGE 1 TO WHATEVER VALUE WAS STORED
179 ON THE STACK BEFORE THE ROUTINE WAS CALLED. THIS ALLOWS A
180 REFERENCE TO PAGE 1 TO TEST THE DESIRED ACCESS KEY. FINALLY,
181 KERNEL PAGE 7 IS MAPPED TO THE EXTERNAL BANK.
182
183 5.3 PROGRAM AND/OR OPERATOR ACTION
184
185 5.3.1 SA 200 (NORMAL DIAGNOSTIC OPERATION)
186
187 THE PROGRAM EXECUTES SEVERAL TESTS OF EACH KEY.
188 TESTS 5 THRU 10 ARE CYCLED THRU 3 TIMES,
189 ONCE FOR EACH OF THE KEYS WHICH GIVES A NON-RESIDENT ABORT.
190 AT THE END OF EACH PASS THRU THE DIAGNOSTIC THE BELL IS RUNG.
191
192 5.3.2 SA 210 (SINGLE SUBTEST LOOP)
193
194 THIS STARTING ADDRESS ALLOWS THE USER TO RUN A SINGLE SUBTEST
195 REPEATEDLY BY GIVING THE ADDRESS OF THE DESIRED SUBTEST AT THE
196 IF SW11 IS SET TO A ONE, NORMAL TEST EXECUTION
197 WILL BE RESUMED AFTER THE SUBTEST IS RUN.
198
199 6.0 ERRORS
200
201 6.1 ERROR PRINTOUT
202
203 PRINTOUTS ARE IN A STANDARD TWO-WORD FORMAT. THE FIRST WORD IS
204 THE OCTAL VALUE OF THE PC+2 OF THE DETECTED ERROR. THE SECOND IS
205 THE CONTENTS OF THE PROCESSOR STATUS REGISTER WHEN THE ERROR WAS
206 DETECTED.

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6.2 ERROR RECOVERY

IN GENERAL, TEST FAILURES WILL PRINTOUT AN ERROR MESSAGE AND CONTINUE. IF THE "HALT ON ERROR" SWITCH IS SET, HITTING CONTINUE WILL RECOVER. IF THE PROGRAM HANGS UP IN A LOOP, THE ERROR IS LIKELY TO BE A SIGNAL WHICH WAS NEVER RECEIVED. IF A HALT OCCURS IN THE TRAP AND VECTOR AREA THE PROGRAM MUST BE RESTARTED. IF THE PROGRAM HALTS IN THE MAIN FLOW, CONSULT THE LISTING IF NO MESSAGE IS TYPED OUT.

7.0 RESTRICTIONS

PROGRAM MUST BE LOADED INTO LOWER 5K OF MEMORY.

8.0 MISCELLANEOUS

8.1 EXECUTION TIME

EACH PASS TAKES APPROXIMATELY 1 MINUTE WITH CORE MEMORY.

9.0 PROGRAM DESCRIPTION

THE PROGRAM RUNS SEVERAL SEPARATE TESTS OF EACH ACCESS KEY. DATI, DAT0 (NO DATIP), DATIP-DATO, AND DATIP-DATOB ARE CHECKED FOR EACH KEY, WITH AND WITHOUT MEMORY MANAGEMENT ENABLE SET. THE BELL IS RUNG AT THE END OF EACH PASS.

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239          /COPYRIGHT 1972, DIGITAL EQUIPMENT CORP., MAYNARD, MASS. 01754
240          /TEST OF THE MEMORY MANAG. ACCESS KEYS
241
242          /THIS PROGRAM IS A MODIFIED 11/40 DIAGNOSTIC, DBKTB. THIS VERSION
243          /HAS BEEN MODIFIED TO INCLUDE SOFTWARE SWITCH REGISTER CAPABILITIES
244          /AND TO ACCOUNT FOR ANY 11/40 - 11/34 DIFFERENCES.
245          /THIS PROGRAM IS INTENDED ONLY FOR USE ON AN 11/34.
246
247          /OPERATING INSTRUCTIONS
248          /      1. LOAD TEST USING THE ABSOLUTE LOADER
249          /      2. SET SR TO INITIAL SETTINGS (USE LOC. 176 FOR SOFTWARE SWR IF NECESSARY)
250          /      3. START AT 200.
251
252          /DYNAMIC SWITCH REGISTER SETTINGS ARE:
253          /SW15=1 CAUSES HALT ON ERROR
254          /SW14=1 CAUSES SCOPE LOOPING
255          /SW13=1 INHIBITS ERROR PRINTOUT
256          /SW11=1 INHIBITS ITERATIONS
257          /SW10=1 HALT AT END OF CURRENT TEST WITH NEXT TEST NUMBER
258          /      IN R0. PRESS CONTINUE TO ADVANCE TO NEXT TEST.
259
260          /DEFINITIONS
261          SCOPE=TRAP
262          NOP=240
263          R0=X0
264          R1=X1
265          R2=X2
266          R3=X3
267          R4=X4
268          R5=X5
269          R6=X6
270          R7=X7
271          SP=X6
272          PC=X7
273          PS=177776
274          STATUS=PS
275          HLT=104006
276
277          /LOAD TRAP CATCHER IN LOCATIONS 0 THRU 377
278          /EACH VECTOR ADDRESS IS LOADED WITH THE ADDRESS
279          /OF THE NEXT LOCATION, AND THE NEXT LOCATION IS LOADED
280          /WITH A HALT INSTRUCTION (000000)
281
282          /LOAD VECTOR AREA
283          .#30
284          000030 006402      EMTSRV
285          000032 000340      340
286          .#34
287          000034 005706      SCOPEC
288          000036 000000      0
289          .#46
290          000046 005430      LOGIC          /ACT HOOKS
291          .#176
292          000176 000000      SWREG: 0          /SOFTWARE SWITCH REGISTER
  
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293
294
295          /LOAD STARTING AREA
296          .#200
297          000200 000167 001746      JMP      START
298          000210
299          000210 000167 005404      JMP      TESTX
300
301          /LOAD DATA AREA
302          .#1000
303          001000 000000      KSTACK: 0
304          002000
305          002000 000000      USTACK: 0
306          002002 000000 000000 000000      .WORD 0,0,0,0
307          002010 000000
308          002012 177564      TCSR: 177564          /TELETYPE PRINTER CSR
309          002014 177566      TDBR: 177566
310          002016 177572      SR0: 177572          /MEMORY MANAG. STATUS REGISTER ADDRESSES
311          002020 177574      SR1: 177574
312          002022 177576      SR2: 177576
313          002024 000250      KTEC: 250          /MEMORY MANAG. INTERRUPT VECTOR
314          002026 000252      KTSTA: 252
315          002030      ADRTAB:
316          002030 177600      UPDR0: 177600          /USER PAGE DESCRIPTOR REGISTER ADDRESSES
317          002032 177602      UPDR1: 177602
318          002034 177604      UPDR2: 177604
319          002036 177606      UPDR3: 177606
320          002040 177610      UPDR4: 177610
321          002042 177612      UPDR5: 177612
322          002044 177614      UPDR6: 177614
323          002046 177616      UPDR7: 177616
324          002050 177640      UPAR0: 177640          /USER PAGE ADDRESS REGISTER ADDRESSES
325          002052 177642      UPAR1: 177642
326          002054 177644      UPAR2: 177644
327          002056 177646      UPAR3: 177646
328          002060 177650      UPAR4: 177650
329          002062 177652      UPAR5: 177652
330          002064 177654      UPAR6: 177654
331          002066 177656      UPAR7: 177656
332          002070 172300      KPDR0: 172300          /KERNEL PAGE DESCRIPTOR REGISTER ADDRESSES
333          002072 172302      KPDR1: 172302
334          002074 172304      KPDR2: 172304
335          002076 172306      KPDR3: 172306
336          002100 172310      KPDR4: 172310
337          002102 172312      KPDR5: 172312
338          002104 172314      KPDR6: 172314
339          002106 172316      KPDR7: 172316
340          002110 172340      KPAR0: 172340          /KERNEL PAGE ADDRESS REGISTER ADDRESSES
341          002112 172342      KPAR1: 172342
342          002114 172344      KPAR2: 172344
343          002116 172346      KPAR3: 172346
344          002120 172350      KPAR4: 172350
345          002122 172352      KPAR5: 172352
346          002124 172354      KPAR6: 172354
  
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347 002126 172356 KPAR7: 172356
348 002126 002126 ADREND# ,+2
349 002130 000000 FTITLE: 0 ;TITLE PRINTED FLAG
350 002132 177573 SR0H: 177573 ;MEMORY MANAG. STATUS REGISTER HIGH BYTE ADDRESSES
351 002134 177575 SR1H: 177575
352 002136 177577 SR2H: 177577
353 002140 000000 NRCONT: 0 ;COUNTER FOR TEST OF THE 3 NR KEYS
354 002142 000000 000004 NRKEYS: 0,4 ;VALUES OF THE 3 NON RESIDENT KEYS
355 002146 125252 DESTAD: 125252 ;LOCATION USED FOR READS AND WRITES TO CHECK
356 002150 177570 SR: 177570 ;SWITCH REG. POINTER
357
358 ;EXECUTION OR ABORTING AT CORRECT POINT
359
360
361 ;SET UP FOR START OF TESTS
362 002152 005037 177776 START: CLR #PS
363 002156 012706 001000 MOV #KSTACK,SP ;SETUP KERNEL STACK
364 002162 012737 140000 177776 MOV #140000,#PS ;SETUP USER STACK POINTER
365 002170 012706 002000 MOV #USTACK,SP
366 002174 005037 177776 CLR #PS
367
368 002200 013746 000004 MOV #4,-(SP) ;SAVE ERROR VECTOR
369 002204 013746 000006 MOV #6,-(SP)
370 002210 012767 002224 175566 MOV #15,4 ;SET UP TIME OUT VECTOR
371 002216 005777 177726 TST #SR ;TRY TO REFERENCE HARDWARE SR
372 002222 000404 BR 28 ;BRANCH IF NO TIMEOUT TRAP OCCURS
373 002224 012767 000176 177716 18: MOV #SWREG,SR ;POINT TO SOFTWARE SR
374 002232 022626 CMP (SP)+,(SP)+ ;RESTORE STACK
375 002234 012637 000006 29: MOV (SP)+,#6 ;RESTORE ERROR VECTOR
376 002240 012637 000004 MOV (SP)+,#4
377 002244 012767 002000 003526 MOV #200,ICOUNT ;INITIALIZE ITERATION COUNT
378 002252 012767 002326 003524 MOV #TEST1+2,RETURN ;SETUP SCOPE AND ITERATION LOOP RETURN
379 002260 005067 177654 CLR NRCONT ;INITIALIZE FOR NR TEST
380 002264 012767 000001 004374 MOV #1,TESTCT ;SET UP TEST SEQUENCE
381 002272 005767 177632 TST FTITLE ;TITLE PRINTED
382 002276 001013 BNE TEST1+2 ;YES, SKIP
383 002300 004767 004164 JSR PC,CRLF ;PRINT TITLE
384 002304 004767 004212 JSR PC,TYPE
385 002310 005404 MTT
386 002312 004767 004152 JSR PC,CRLF
387 002316 005267 177606 INC FTITLE
388 002322 000401 BR ,+4

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389
390 ;SHOW THAT DATI TO A RRO PAGE (ACF=2) NEITHER TRAPS NOR ABORTS
391 ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS CONTINUE TO TRACK, AND THAT
392 ;THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
393 TEST1: SCOPE
394 002324 104400 MOV #KSTACK,SP ;INITIALIZE KERNEL STACK POINTER
395 002332 005077 177460 CLR #SR0 ;INITIALIZE SR0
396 002336 004767 004240 JSR PC,ORDER ;CHECK TEST SEQUENCE
397 002342 000001 I ;TEST NUMBER
398 002344 104006 HLT ;TEST EXECUTED OUT OF SEQUENCE
399 002346 012746 000002 MOV #2,-(SP) ;PUSH RRO KEY ON STACK
400 002352 004767 003212 JSR ;7,SETUP ;MAKE KERNEL PAGE 1 RRO, BANK 0
401 ;MAKE KERNEL PAGE 7 RW, EXTERNAL
402 ;MAKE ALL OTHER PAGES RW, BANK 0
403 002356 005726 TST (SP)+ ;RESTORE STACK
404 002360 012777 002474 177436 MOV #RET1,#KTVEC ;SETUP ABORT RETURN IN CASE
405 002366 005077 177434 CLR #KTSTA
406 002372 012767 125252 177546 MOV #125252,DESTAD ;SETUP LOCATION TO BE REFERENCED
407 002400 012781 022146 MOV #DESTAD+20000,R1 ;R1 CONTAINS VIRTUAL ADDRESS OF LOCATION TO
408 ;BE REFERENCED THRU KERNEL PAGE 1
409 002404 005277 177406 INC #SR0 ;TURN ON MEMORY MANAG.
410 002410 022721 125252 CMP #125252,(R1)+ ;DATI TO RRO PAGE
411 002414 001484 BEQ CMPDK1 ;BRANCH IF CORRECT VALUE WAS READ
412 002416 005377 177374 DEC #SRW ;ON ERROR, TURN OFF MEMORY MANAG.
413 002422 104006 HLT ;RELOCATION FAILED THRU KERNEL PAGE 1
414 002424 000427 BR
415 002426 017702 177364 CMPDK1: MOV #SR0,R2 ;SAVE CONTENTS OF SR0
416 002432 105377 177360 DECB #SR0 ;TURN OFF MEMORY MANAG.
417 002436 022702 000017 CMP #17,R2 ;CHECK SAVED CONTENTS OF SR0
418 002442 001401 BEQ ,+4
419 002444 104006 HLT ;SR0 INCORRECT-SHOULD HAVE
420 ;TRACKED REFERENCE TO PAGE 0,
421 ;WHICH GOT THE ADDRESS OF SR0
422 002446 022777 002446 177346 CMP #.,#SR2 ;CHECK SR2
423 002454 001401 BEQ ,+4
424 002456 104006 HLT ;SR2 INCORRECT-SHOULD TRACK EVEN
425 ;WHEN MEMORY MANAG. IS OFF
426 002460 022777 077402 177404 CMP #77402,#KPDR1 ;CHECK PDR FOR
427 002466 001401 BEQ ;THE RRO PAGE REFERENCED
428 002470 104006 HLT ;KPDR1 INCORRECT-SHOULD NOT
429 ;HAVE BEEN CHANGED
430 002472 000404 BR
431 002474 042777 000001 177314 RET1: BIC #1,#SR0 ;TURN OFF MEMORY MANAG.
432 002502 104006 HLT ;DATI TO RRO PAGE CAUSED
433 ;A TRAP OR ABORT
434 002504 016777 177316 177312 DONE1: MOV #KTSTA,#KTVEC ;RESTORE TRAP RETURN TO CAUSE HALT
435 002512 005077 177310 CLR #KTSTA ;ON AN UNEXPECTED TRAP
436 002516 005077 177274 CLR #SR0 ;INITIALIZE SR0
437 002522 005037 177776 CLR #PS ;INITIALIZE PROCESSOR STATUS
438
439 ;SHOW THAT A DATO (NO DATIP) TO A RRO PAGE (ACF=2) ABORTS
440 ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS LOCK UP, AND THAT THE PDR
441 ;CORRESPONDING TO THE REFERENCE IS CORRECT
442 TEST2: SCOPE

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443 002530 012706 001000      MOV    #KSTACK,SP      INITIALIZE KERNEL STACK POINTER
444 002534 005077 177256      CLR    #SR0            INITIALIZE SR0
445 002540 004767 004036      JSR    PC,ORDER       CHECK TEST SEQUENCE
446 002544 000002 2                JTEST NUMBER
447 002546 104006                HLT                    TEST EXECUTED OUT OF SEQUENCE
448 002550 012746 000002      MOV    #2,-(SP)       PUSH RRO KEY ON STACK
449 002554 004767 003010      JSR    X7,SETUP       MAKE KERNEL PAGE 1 RRO, BANK 0
450                                MAKE KERNEL PAGE 7 RW, EXTERNAL
451                                MAKE ALL OTHER PAGES RW, BANK 0
452 002560 005726                TST    (SP)+          RESTORE STACK POINTER
453 002562 012777 002626 177234  MOV    #RET4,#KTVEC   SETUP ABORT RETURN
454 002570 005077 177232      CLR    #KTSTA
455 002574 005067 177346      CLR    DESTAD         INITIALIZE LOCATION TO BE ADDRESSED
456                                BY DATO TO RRO PAGE
457 002600 012702 022146      MOV    #DESTAD+20000,R2  R2 CONTAINS ADDRESS OF LOCATION
458                                TO BE REFERENCED THRU KERNEL PAGE 1
459 002604 012777 000001 177204  MOV    #1,#SR0        TURN ON MEMORY MANAG.
460 002612 012722 125252      MOV    #125252,(R2)+  DATO TO RRO PAGE-SHOULD ABORT
461 002616 005377 177174      DEC    #SR0          TURN OFF MEMORY MANAG.
462 002622 104006                HLT                    DATO TO RRO PAGE FAILED TO ABORT
463 002624 000426                BR     DONE4
464 002626 017701 177164      MOV    #SR0,R1       SAVE CONTENTS OF SR0
465 002632 005377 177160      DEC    #SR0          TURN OFF MEMORY MANAG.
466 002636 022701 020003      CMP    #20003,R1     CHECK SAVED CONTENTS OF SR0
467 002642 001401                BEQ    .+4
468 002644 104006                HLT                    SR0 INCORRECT-SHOULD HAVE LOCKED
469                                ON DATO TO KERNEL PAGE 1(RRO)
470                                AND ACCESS FAULT SHOULD BE SET
471 002646 022777 002612 177146  CMP    #AD4,#SR2     CHECK SR2
472 002654 001401                BEQ    .+4
473 002656 104006                HLT                    SR2 INCORRECT-SHOULD HAVE LOCKED
474                                ON THE ABORTED REFERENCE, WITH THE
475                                VIRTUAL ADDRESS OF THE INSTRUCTION
476 002660 022777 077402 177204  CMP    #77402,#KPDR1 CHECK INSTRUCTION SPACE PDR
477 002666 001401                BEQ    .+4
478 002670 104006                HLT                    KPDR1 INCORRECT-SHOULD NOT
479                                HAVE BEEN CHANGED SINCE THE
480                                DATO DIDN'T WRITE
481 002672 005767 177250      TST    DESTAD        MAKE CERTAIN THAT DESTINATION
482 002676 001401                BEQ    .+4            LOCATION WAS NOT WRITTEN
483 002700 104006                HLT                    DATO TO RRO PAGE WROTE
484                                INTO THE DESTINATION LOCATION
485 002702 016777 177120 177114  DONE4: MOV    KTSTA,#KTVEC  CHANGE MEMORY MANAG. TRAP RETURN
486 002710 005077 177112      CLR    #KTSTA        TO CAUSE A HALT ON AN UNEXPECTED TRAP
487 002714 005077 177076      CLR    #SR0
488 002720 005037 177776      CLR    #PS
489
490                                ISHOW THAT A DATIP, DATO SEQUENCE TO A RRO PAGE (ACP=2) ABORTS
491                                ISHOW THAT THE MEMORY MANAG. STATUS REGISTERS LOCK UP, AND THAT THE PDR
492                                CORRESPONDING TO THE REFERENCE IS CORRECT
493                                TEST3: SCOPE
494 002724 104400                MOV    #KSTACK,SP    INITIALIZE KERNEL STACK POINTER
495 002726 012706 001000      CLR    #SR0          INITIALIZE SR0
496 002732 005077 177060      JSR    PC,ORDER       CHECK TEST SEQUENCE
497 002736 004767 003640

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497 002742 000003                3                JTEST NUMBER
498 002744 104006                HLT                    TEST EXECUTED OUT OF SEQUENCE
499 002746 012746 000002      MOV    #2,-(SP)       PUSH RRO KEY ON STACK
500 002752 004767 002612      JSR    X7,SETUP       MAKE KERNEL PAGE 1 RRO, BANK 0
501                                MAKE KERNEL PAGE 7 RW, EXTERNAL
502                                MAKE ALL OTHER PAGES RW, BANK 0
503 002756 005726                TST    (SP)+          RESTORE STACK POINTER
504 002760 012777 003024 177036  MOV    #RET5,#KTVEC   SETUP ABORT RETURN
505 002766 005077 177034      CLR    #KTSTA
506 002772 005067 177150      CLR    DESTAD         INITIALIZE LOCATION TO BE ADDRESSED
507                                BY DATIP,DATO TO RRO PAGE
508 002776 012703 022150      MOV    #DESTAD+20002,R3  R3 CONTAINS VIRTUAL ADDRESS+2 OF LOCATION
509                                TO BE REFERENCED THRU KERNEL PAGE 1
510 003002 052777 000001 177006  BIS    #1,#SR0        TURN ON MEMORY MANAG.
511 003010 005243                INC    =(R3)         DATIP, DATO TO RRO PAGE
512 003012 042777 000001 176776  BIC    #1,#SR0        TURN OFF MEMORY MANAG.
513 003020 104006                HLT                    DATIP, DATO TO RRO PAGE FAILED TO
514 003022 000427                BR     DONE5
515 003024 017701 176766      MOV    #SR0,R1       SAVE CONTENTS OF SR0
516 003030 042777 000001 176760  BIC    #1,#SR0        TURN OFF MEMORY MANAG.
517 003036 022701 020003      CMP    #20003,R1     CHECK SAVED CONTENTS OF SR0
518 003042 001401                BEQ    .+4
519 003044 104006                HLT                    SR0 INCORRECT-SHOULD HAVE LOCKED
520                                ON DATO TO KERNEL PAGE 1(RRO) AND
521                                ACCESS FAULT SHOULD BE SET
522 003046 022777 003010 176746  CMP    #AD5,#SR2     CHECK SR2
523 003054 001401                BEQ    .+4
524 003056 104006                HLT                    SR2 INCORRECT-SHOULD HAVE LOCKED
525                                ON THE ABORTED REFERENCE, WITH THE
526                                VIRTUAL ADDRESS OF THE INSTRUCTION
527 003060 022777 077402 177004  CMP    #77402,#KPDR1 CHECK PDR
528 003066 001401                BEQ    .+4
529 003070 104006                HLT                    KPDR1 INCORRECT - SHOULD NOT HAVE
530                                BEEN CHANGED, SINCE DATIP IS ABORTED
531                                SINCE IT WILL BE FOLLOWED BY A DATO OR DATOB
532 003072 005767 177050      TST    DESTAD        MAKE CERTAIN THAT DESTINATION
533 003076 001401                BEQ    .+4            LOCATION WAS NOT WRITTEN
534 003100 104006                HLT                    DATO TO RRO PAGE WROTE INTO
535                                THE DESTINATION LOCATION
536 003102 016777 176720 176714  DONE5: MOV    KTSTA,#KTVEC  CHANGE PAGE FAULT RETURN
537 003110 005077 176712      CLR    #KTSTA        TO CAUSE A HALT ON AN UNEXPECTED
538 003114 005077 176676      CLR    #SR0          TRAP
539 003120 005037 177776      CLR    #PS
540
541                                ISHOW THAT A DATIP,DATOB SEQUENCE TO A RRO PAGE (ACP=2) WORD ABORTS
542                                ISHOW THAT THE MEMORY MANAG. STATUS REGISTERS LOCK UP, AND THAT THE PDR
543                                CORRESPONDING TO THE REFERENCE IS CORRECT
544                                TEST4: SCOPE
545 003124 104400                MOV    #KSTACK,SP    INITIALIZE KERNEL STACK POINTER
546 003126 012706 001000      CLR    #SR0          INITIALIZE SR0
547 003132 005077 176660      JSR    PC,ORDER       CHECK TEST SEQUENCE
548 003136 004767 003440      JSR    PC,ORDER
549 003142 000004                4                JTEST NUMBER
550 003144 104006                HLT                    TEST EXECUTED OUT OF SEQUENCE
551 003146 012746 000002      MOV    #2,-(SP)       PUSH RRO KEY ON STACK

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551 003152 004767 002412 JSR X7,SETUP ;MAKE KERNEL PAGE 1 RRO, BANK 0
552 ;MAKE KERNEL PAGE 7 RW, EXTERNAL
553 ;MAKE ALL OTHER PAGES RW, BANK 0
554 003156 005726 TST (SP)+ ;RESTORE STACK POINTER
555 003160 012777 003222 176636 MOV #RET6,#KTVEC ;SETUP ABORT RETURN
556 003166 005077 176634 CLR #KTSTA
557 003172 005067 176750 CLR DESTAD ;INITIALIZE LOCATION TO BE ADDRESSED
558 ;BY DATIP,DATOB TO RRO PAGE
559 003176 012704 022146 MOV #DESTAD+20000,R4 ;R4 CONTAINS VIRTUAL ADDRESS OF LOCATION
560 ;TO BE REFERENCED THRU KERNEL PAGE 1
561 003202 052777 000001 176606 BIS #1,#SR0 ;TURN ON MEMORY MANAG.
562 003210 105224 AD6: INCB (R4)+ ;DATIP, DATOB TO RROT PAGE
563 003212 005377 176600 DEC #SR0 ;TURN OFF MEMORY MANAG.
564 003216 104006 HLT ;DATIP,DATO TO RROT PAGE FAILED TO ABORT
565 003220 000426 BR DONE6
566 003222 017701 176570 RET6: MOV #SR0,R1 ;SAVE CONTENTS OF SR0
567 003226 005377 176564 DEC #SR0 ;TURN OFF MEMORY MANAG.
568 003232 022701 020003 CMP #20003,R1 ;CHECK SAVED CONTENTS OF SR0
569 003236 001401 BEQ .+4
570 003240 104006 HLT ;SR0 INCORRECT-SHOULD HAVE LOCKED ON
571 ;DATO TO KERNEL PAGE 1 (RRO)
572 ;ACCESS FAULT SHOULD BE SET
573 003242 022777 003210 176552 CMP #AD6,#SR2 ;CHECK SR2
574 003250 001401 BEQ .+4
575 003252 104006 HLT ;SR2 INCORRECT-SHOULD HAVE LOCKED
576 ;ON THE ABORTED REFERENCE, WITH THE
577 ;VIRTUAL ADDRESS OF THE INSTRUCTION
578 003254 022777 077402 176610 CMP #77402,#KPDR1 ;CHECK PDR
579 003262 001401 BEQ .+4
580 003264 104006 HLT ;KPDR1 INCORRECT - SHOULD NOT HAVE
581 ;BEEN CHANGED-DATIP IS ABORTED
582 ;SINCE IT MUST BE FOLLOWED BY A DATO
583 003266 005767 176654 TST DESTAD ;MAKE CERTAIN THAT DESTINATION
584 003272 001401 BEQ .+4 ;LOCATION HAS NOT WRITTEN
585 003274 104006 HLT ;DATO TO RRO PAGE WROTE INTO
586 ;THE DESTINATION LOCATION
587 003276 016777 176524 176520 DONE6: MOV KTSTA,#KTVEC ;CHANGE MEMORY MANAG. FAULT
588 003304 005077 176516 CLR #KTSTA ;RETURN TO CAUSE A HALT ON AN
589 003310 005077 176502 CLR #SR0 ;UNEXPECTED TRAP
590 003314 005037 177776 CLR #PS
591
592 ;THE FOLLOWING TESTS (5-10) ARE RUN FOR BOTH OF THE NON-RESIDENT
593 ;KEYS - A PASS IS MADE FOR KEY 0, THEN A PASS IS MADE FOR KEY 4,
594 ;THE CURRENT KEY IS STORED ON THE STACK
595 ;SHOW THAT DATI TO A NR PAGE ABORTS WITHOUT COMPLETING
596 ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS LOCK UP, AND THAT
597 ;THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
598 003320 104400 TEST5: SCOPE
599 003322 012706 001000 MOV #KSTACK,SP ;INITIALIZE KERNEL STACK POINTER
600 003326 005077 176464 CLR #SR0 ;INITIALIZE SR0
601 003332 004767 003244 JSR PC,ORDER ;CHECK TEST SEQUENCE
602 003336 000005 S ;TEST NUMBER
603 003340 104006 HLT ;TEST EXECUTED OUT OF SEQUENCE
604 003342 005037 001000 CLR #KSTACK ;PUT 0 ON STACK AS FIRST NR KEY TO BE TESTED

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605 ;THIS INSTRUCTION IS SKIPPED WHEN TESTING THE
606 ;OTHER WHICH IS SETUP AFTER TEST30
607 003346 012706 001000 RERUN: MOV #KSTACK,SP
608 003352 005077 176440 CLR #SR0
609 003356 004767 002206 JSR X7,SETUP ;MAKE KERNEL PAGE 1 NR, BANK 0
610 ;MAKE KERNEL PAGE 7 RW, EXTERNAL
611 ;MAKE ALL OTHER PAGES RW, BANK 0
612 003362 012777 003426 176434 MOV #RET21,#KTVEC ;SETUP ABORT RETURN
613 003370 005077 176432 CLR #KTSTA
614 003374 005003 R3 ;INITIALIZE DESTINATION LOCATION
615 003376 012767 125252 176542 MOV #125252,DESTAD ;INITIALIZE SOURCE LOCATION
616 003404 012701 022146 MOV #DESTAD+20000,R1 ;R1 CONTAINS VIRTUAL ADDRESS OF LOCATION
617 ;TO BE REFERENCED THRU KERNEL PAGE 1
618 003410 005277 176402 INC #SR0 ;TURN ON MEMORY MANAG.
619 003414 012103 AD21: MOV (R1)+,R3 ;DATI TO NR PAGE - SHOULD ABORT
620 003422 005377 176374 DEC #SR0 ;ON ERROR, TURN OFF MEMORY MANAG.
621 003426 104006 HLT ;NO ABORT ON DATI TO A NON-RESIDENT PAGE
622 003428 004300 BR DONE21
623 003426 017702 176364 RET21: MOV #SR0,R2 ;SAVE CONTENTS OF SR0
624 003432 105377 176360 DECB #SR0 ;TURN OFF MEMORY MANAG.
625 003436 022702 100003 CMP #100003,R2 ;CHECK SAVED CONTENTS OF SR0
626 003442 001401 BEQ .+4
627 003444 104006 HLT ;SR0 INCORRECT-SHOULD HAVE
628 ;LOCKED ON REFERENCE TO
629 ;KERNEL PAGE 1 WHICH WAS NON-RESIDENT
630 003446 022777 003414 176346 CMP #AD21,#SR2 ;CHECK SR2
631 003454 001401 BEQ .+4
632 003456 104006 HLT ;SR2 INCORRECT-SHOULD HAVE LOCKED ON
633 ;NR REFERENCE
634 003460 017705 176406 MOV #KPDR1,R5 ;MOVE CONTENTS OF KPDR1 TO R5
635 003464 042705 000007 BIC #7,R5 ;TO MASK OFF ACCESS KEY
636 003470 022705 077400 CMP #77400,R5 ;CHECK PDR FOR
637 003474 001401 BEQ .+4 ;THE NR PAGE REFERENCED (BITS 0-2 MASKED OUT)
638 003476 104006 HLT ;KPDR1 INCORRECT-SHOULD NOT
639 ;HAVE BEEN CHANGED
640 003500 005703 TST R3 ;CHECK DESTINATION LOCATION TO SEE
641 003502 001401 BEQ .+4 ;IF INSTRUCTION ALTERED IT BEFORE ABORTING
642 003504 104006 HLT ;INSTRUCTION COMPLETED BEFORE ABORT OCCURRED
643 003506 016777 176314 176310 DONE21: MOV KTSTA,#KTVEC ;RESTORE TRAP RETURN TO CAUSE HALT
644 003514 005077 176306 CLR #KTSTA ;ON AN UNEXPECTED TRAP
645 003520 005077 176272 CLR #SR0 ;INITIALIZE SR0
646 003524 005037 177776 CLR #PS ;INITIALIZE PROCESSOR STATUS
647
648 ;SHOW THAT A DATO (NO DATIP) TO A NR PAGE
649 ;ABORTS WITHOUT COMPLETING THE DATO
650 ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS LOCK UP, AND THAT THE PDR
651 ;CORRESPONDING TO THE REFERENCE IS CORRECT
652 003530 104400 TEST6: SCOPE
653 003532 012706 001000 MOV #KSTACK,SP ;INITIALIZE KERNEL STACK POINTER
654 003536 005077 176294 CLR #SR0 ;INITIALIZE SR0
655 003542 004767 003034 JSR PC,ORDER ;CHECK TEST SEQUENCE
656 003546 000006 S ;TEST NUMBER
657 003550 104006 HLT ;TEST EXECUTED OUT OF SEQUENCE
658 003552 004767 002012 JSR X7,SETUP ;MAKE KERNEL PAGE 1 NR, BANK 0

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659                                     ;MAKE KERNEL PAGE 7 RW, EXTERNAL
660                                     ;MAKE ALL OTHER PAGES RW, BANK 0
661 003556 012777 003624 176240      MOV    #RET23,#KTVEC
662 003564 005077 176236              CLR    #KTSTA
663 003570 005067 176352              CLR    DESTAD
664                                     ;INITIALIZE LOCATION TO BE ADDRESSED
665 003574 012701 022146              MOV    #DESTAD+20000,R1
666                                     ;R1 CONTAINS ADDRESS OF LOCATION
667 003600 112777 000001 176210      MOV    #1,#SR0
668 003606 012721 125252              AD23: MOV    #125252,(R1)+
669 003612 042777 000001 176176      BIC    #1,#SR0
670 003620 104006                      HLT
671 003622 000431                      BR
672 003624 017702 176166              RET23: MOV    DONE23
673 003630 005377 176162              MOV    #SR0,R2
674 003634 022702 100003              DEC    #SR0
675 003640 001401                      CMP    #100003,R2
676 003642 104006                      BEQ
677                                     ;SR0 INCORRECT-SHOULD HAVE LOCKED
678                                     ;ON DATO TO KERNEL PAGE 1(NR)
679 003644 022777 003606 176150      CMP    #AD23,#SR2
680 003652 001401                      BEQ
681 003654 104006                      HLT
682                                     ;SR2 INCORRECT-SHOULD HAVE LOCKED
683                                     ;ON THE ABORTED REFERENCE, CONTAINING THE
684 003656 017703 176210              MOV    #KPDR1,R3
685 003662 042703 000007              BIC    #7,R3
686 003666 022703 077400              CMP    #77400,R3
687 003672 001401                      BEQ
688 003674 104006                      HLT
689                                     ;CHECK PDR
690 003676 005767 176244              TST   DESTAD
691 003702 001401                      BEQ
692 003704 104006                      HLT
693                                     ;LOCATION WAS NOT WRITTEN
694 003706 016777 176114 176110     DONE23: MOV  #KTSTA,#KTVEC
695 003714 005077 176106              CLR    #KTSTA
696 003720 005077 176072              CLR    #SR0
697 003724 005037 177776              CLR    #PS
698
699                                     ;SHOW THAT A DATIP, DATO SEQUENCE TO A NR PAGE WORD ABORTS
700                                     ;SHOW THAT THE MEMORY MANAG, STATUS REGISTERS LOCK UP, AND THAT THE PDR
701                                     ;CORRESPONDING TO THE REFERENCE IS CORRECT
702 003730 104400                      TEST7: SCOPE
703 003732 012706 001000              MOV    #KSTACK,SP
704 003736 005077 176054              CLR    #SR0
705 003742 004767 002634              JSR    PC,ORDER
706 003746 000007                      7
707 003750 104006                      HLT
708 003752 004767 001612              JSR    X7,SETUP
709
710                                     ;MAKE KERNEL PAGE 1 NR,BANK 0
711 003756 012777 004022 176040      MOV    #RET25,#KTVEC
712 003764 005077 176036              CLR    #KTSTA

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713 003770 005067 176152              CLR    DESTAD
714                                     ;INITIALIZE LOCATION TO BE ADDRESSED
715 003774 012703 022150              MOV    #DESTAD+20002,R3
716                                     ;R3 CONTAINS ADDRESS+2 OF LOCATION
717 004000 052777 000001 176010      BIS    #1,#SR0
718 004006 005243                      AD25: INC    -(R3)
719 004010 042777 000001 176000      BIC    #1,#SR0
720 004016 104006                      HLT
721 004020 000432                      BR
722 004022 017701 175770              RET25: MOV    DONE25
723 004026 042777 000001 175762      MOV    #SR0,R1
724 004034 022701 100003              BIC    #1,#SR0
725 004040 001401                      CMP    #100003,R1
726 004042 104006                      BEQ
727                                     ;SR0 INCORRECT-SHOULD HAVE LOCKED
728                                     ;ON DATO TO KERNEL PAGE 1(NR)
729 004044 022777 004006 175750      CMP    #AD25,#SR2
730 004052 001401                      BEQ
731 004054 104006                      HLT
732                                     ;SR2 INCORRECT-SHOULD HAVE LOCKED
733                                     ;ON THE ABORTED REFERENCE, CONTAINING THE
734 004056 017704 176010              MOV    #KPDR1,R4
735 004062 042704 000007              BIC    #7,R4
736 004066 022704 077400              CMP    #77400,R4
737 004072 001401                      BEQ
738 004074 104006                      HLT
739                                     ;CHECK PDR
740 004076 005767 176044              TST   DESTAD
741 004102 001401                      BEQ
742 004104 104006                      HLT
743                                     ;LOCATION WAS NOT WRITTEN
744 004106 016777 175714 175710     DONE25: MOV  #KTSTA,#KTVEC
745 004114 005077 175706              CLR    #KTSTA
746 004120 005077 175672              CLR    #SR0
747 004124 005037 177776              CLR    #PS
748
749                                     ;SHOW THAT A DATIP,DATOB SEQUENCE TO A NR PAGE WORD ABORTS
750                                     ;SHOW THAT THE MEMORY MANAG, STATUS REGISTERS LOCK UP, AND THAT THE PDR
751                                     ;CORRESPONDING TO THE REFERENCE IS CORRECT
752 004130 104400                      TEST10: SCOPE
753 004132 012706 001000              MOV    #KSTACK,SP
754 004136 005077 175654              CLR    #SR0
755 004142 004767 002434              JSR    PC,ORDER
756 004146 000010                      10
757 004150 104006                      HLT
758 004152 004767 001412              JSR    X7,SETUP
759
760                                     ;MAKE KERNEL PAGE 1 NR, BANK 0
761 004156 012777 004220 175640      MOV    #RET27,#KTVEC
762 004164 005077 175636              CLR    #KTSTA
763 004170 005067 175752              CLR    DESTAD
764                                     ;INITIALIZE LOCATION TO BE ADDRESSED
765 004174 012704 022146              MOV    #DESTAD+20000,R4
766 004200 052777 000001 175610      BIS    #1,#SR0

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767 004206 105224          AD27: INCB (R4)+      ;DATIP, DATOB TO NR PAGE-SHOULD ABORT
768 004210 005377          DEC      #SR0      ;TURN OFF MEMORY MANAG.
769 004214 104006          HLT                    ;DATIP,DATO TO NR PAGE FAILED
770 004216 000431          BR      DONE27        ;TO ABORT
771 004220 017701 175572  RET27: MOV      #SR0,R1    ;SAVE CONTENTS OF SR0
772 004224 005377          DEC      #SR0      ;TURN OFF MEMORY MANAG.
773 004230 022701 100003  CMP      #100003,R1  ;CHECK SAVED CONTENTS OF SR0
774 004234 001401          BEQ     .+4
775 004236 104006          HLT                    ;SR0 INCORRECT-SHOULD HAVE LOCKED ON
776                                     ;DATIP, DATOB TO KERNEL DATA PAGE 1 (NR)
777                                     ;NR FAULT SHOULD BE SET
778 004240 022777 004206 175554  CMP      #AD27,#SR2  ;CHECK SR2
779 004246 001401          BEQ     .+4
780 004250 104006          HLT                    ;SR2 INCORRECT SHOULD HAVE LOCKED
781                                     ;ON THE ABORTED REFERENCE, CONTAINING THE
782                                     ;VIRTUAL ADDRESS OF THE INSTRUCTION
783 004252 017702 175614          MOV      #KPDR1,R2   ;MOVE CONTENTS OF PDR 1 TO R2
784 004256 042702 000007  BIC      #7,R2        ;TO MASK OFF THE ACCESS KEY
785 004262 022702 077400  CMP      #77400,R2   ;CHECK INSTRUCTION SPACE PDR
786 004266 001401          BEQ     .+4           ;WITH BITS 0-2 MASKED OFF
787 004270 104006          HLT                    ;KPDR1 INCORRECT-SHOULD NOT HAVE
788                                     ;BEEN CHANGED
789 004272 005767 175650          TST     DESTAD       ;MAKE CERTAIN THAT DESTINATION
790 004276 001401          BEQ     .+4           ;LOCATION WAS NOT WRITTEN
791 004300 104006          HLT                    ;DATAB TO NR PAGE WROTE INTO
792                                     ;THE DESTINATION LOCATION
793 004302 016777 175520 175514  DONE27: MOV      KTSTA,#KTVEC  ;CHANGE MEMORY MANAG. FAULT
794 004310 005077 175512          CLR     #KTSTA      ;RETURN TO CAUSE A HALT ON AN
795 004314 005077 175476          CLR     #SR0        ;UNEXPECTED TRAP
796 004320 005037 177776          CLR     #PS
797 004324 104400          SCOPE
798 004326 005267 175606          INC     NRCNT        ;COUNT HOW MANY NR KEYS HAVE BEEN TESTED
799 004332 022767 000002 175600  CMP      #2,NRCNT
800 004340 001416          BEQ     .+4           ;IF ALL 2 HAVE BEEN TESTED, BRANCH
801 004342 016701 175572          MOV      NRCNT,R1    ;OTHERWISE, CALCULATE OFFSET TO GET NEXT KEY
802 004346 006301          ASL     R1
803 004350 016137 002142 001000  MOV      NRKEYS(R1),#KSTACK ;PUT NEXT NR KEY ON STACK
804 004356 012767 003346 001420  MOV      #RERUNA,RETURN ;PUT NEW SCOPE LOOP ADDRESS IN RETURN
805 004364 012767 000005 002274  MOV      #5,TESTCT   ;REINIT TEST COUNTER SEQ
806 004372 000167 176750          JMP     RUNA         ;JUMP TO EXECUTE TESTS WITH NEXT NR KEY
807 004376 005067 175536          CLR     NRCNT
808 004402 012767 001372 004000  MOV      #4000,SCOPEF
809 004410 005367 002252          DEC     TESTCT
810 004414          NXTST1:
811
812                                     ;SHOW THAT DATI TO A RW PAGE (ACF=6)
813                                     ;NEITHER TRAPS NOR ABORTS
814                                     ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS CONTINUE TO TRACK, AND THAT
815                                     ;THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
816 004414 104400          TEST11: SCOPE
817 004416 012706 001000          MOV      #KSTACK,SP ;INITIALIZE KERNEL STACK POINTER
818 004422 005077 175370          CLR     #SR0        ;INITIALIZE SR0
819 004426 004767 002150          JSR     PC,ORDER    ;CHECK TEST SEQUENCE
820 004432 000011          I      1           ;TEST NUMBER
    
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821 004434 104006          HLT                    ;TEST EXECUTED OUT OF SEQUENCE
822 004436 012746 000006          MOV      #6,=(SP)   ;PUSH RW KEY ON STACK
823 004442 004767 001122          JSR     X7,SETUP     ;MAKE KERNEL PAGE 1 RW, BANK 0
824                                     ;MAKE KERNEL PAGE 7 RW, EXTERNAL
825                                     ;MAKE ALL OTHER PAGES RW, BANK 0
826 004446 005726          TST     (SP)+        ;RESTORE STACK POINTER
827 004450 012777 004564 175346  MOV      #RET31,#KTVEC ;SETUP ABORT RETURN IN CASE
828 004456 005077 175344          CLR     #KTSTA
829 004462 012767 125252 175456  MOV      #125252,DESTAD ;INITIALIZE LOCATION TO BE READ
830 004470 012701 022146          MOV      #DESTAD+20000,R1 ;R1 CONTAINS VIRTUAL ADDRESS OF
831                                     ;LOCATION TO BE REFERENCED THRU KERNEL PAGE 1
832 004474 005277 175316          INC     #SR0        ;TURN ON MEMORY MANAG.
833 004500 022721 125252          CMP      #125252,(R1)+ ;DATI TO RW PAGE-SHOULDN'T TRAP OR ABORT
834 004504 001404          BEQ     OK31
835 004506 005377 175304          DEC     #SR0
836 004512 104006          HLT                    ;ON ERROR, TURN OFF MEMORY MANAG.
837 004514 000427          BR      DONE31       ;RELOCATION FAILED THRU KERNEL PAGE 1
838 004516 017702 175274  OK31: MOV      #SR0,R2    ;SAVE CONTENTS OF SR0
839 004522 105377 175270          MOV      #SR0      ;TURN OFF MEMORY MANAG.
840 004526 022702 000017  CMP      #17,R2     ;CHECK SAVED CONTENTS OF SR0
841 004532 001401          BEQ     .+4
842 004534 104006          HLT                    ;SR0 INCORRECT-SHOULD HAVE
843                                     ;TRACKED REFERENCE TO
844                                     ;PAGE 0, WHICH GOT THE ADDRESS
845                                     ;OF SR0 TO TURN OFF MEMORY MANAG.
846 004536 022777 004536 175256  CMP      #.,#SR2    ;CHECK SR2
847 004544 001401          BEQ     .+4
848 004546 104006          HLT                    ;SR2 INCORRECT-SHOULD TRACK EVEN
849                                     ;WHEN MEMORY MANAG. IS OFF
850 004550 022777 077406 175314  CMP      #77406,#KPDR1 ;CHECK PDR FOR
851 004556 001401          BEQ     .+4           ;THE RW PAGE REFERENCED
852 004560 104006          HLT                    ;KPDR1 INCORRECT-SHOULD NOT
853                                     ;HAVE BEEN CHANGED
854 004562 000404          BR      DONE31
855 004564 042777 000001 175224  RET31: BIC      #1,#SR0 ;TURN OFF MEMORY MANAG.
856 004572 104006          HLT                    ;DATI TO RW PAGE CAUSED
857                                     ;A TRAP OR ABORT
858 004574 016777 175226 175222  DONE31: MOV      KTSTA,#KTVEC  ;RESTORE TRAP RETURN TO CAUSE HALT
859 004602 005077 175220          CLR     #KTSTA      ;ON AN UNEXPECTED TRAP
860 004606 005077 175204          CLR     #SR0        ;INITIALIZE SR0
861 004612 005037 177776          CLR     #PS         ;INITIALIZE PROCESSOR STATUS
862
863                                     ;SHOW THAT A DATO (NO DATIP) TO A RW PAGE (ACF=6)
864                                     ;NEITHER TRAPS NOR ABORTS
865                                     ;SHOW THAT THE MEMORY MANAG. STATUS REGISTERS CONTINUE TO TRACK, AND THAT
866                                     ;THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
867 004616 104400          TEST12: SCOPE
868 004620 012706 001000          MOV      #KSTACK,SP ;INITIALIZE KERNEL STACK POINTER
869 004624 005077 175166          CLR     #SR0        ;INITIALIZE SR0
870 004630 004767 001746          JSR     PC,ORDER    ;CHECK TEST SEQUENCE
871 004634 000012          I      12          ;TEST NUMBER
872 004636 104006          HLT                    ;TEST EXECUTED OUT OF SEQUENCE
873 004640 012746 000006          MOV      #6,=(SP)   ;PUSH RW KEY ON THE STACK
874 004644 004767 000720          JSR     X7,SETUP     ;MAKE KERNEL PAGE 1 RW, BANK 0
    
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875                                     ]MAKE KERNEL PAGE 7 RW, EXTERNAL
876                                     ]MAKE ALL OTHER PAGES RW, BANK 0
877 004650 005226 TST (SP)+ ]RESTORE STACK POINTER
878 004652 012777 004764 175144 MOV #RET33,#KTVEC ]SETUP ABORT RETURN IN CASE
879 004660 005077 175142 CLR #KTSTA
880 004664 005067 175256 DESTAD ]INITIALIZE LOCATION TO BE REFERENCED
881 004670 012701 022146 MOV #DESTAD+20000,R1 ]R1 CONTAINS VIRTUAL ADDRESS OF
882 ]LOCATION TO BE REFERENCED THRU KERNEL PAGE 1
883 004674 005277 175116 INC #SR0 ]TURN ON MEMORY MANAG.
884 004700 012721 125252 MOV #125252,(R1)+ ]DATO TO RW PAGE-SHOULDN'T TRAP OR ABORT
885 004704 017702 175106 MOV #SR0,R2 ]SAVE CONTENTS OF SR0
886 004710 105377 175102 CLR #SR0 ]TURN OFF MEMORY MANAG.
887 004714 022702 000017 CMP #17,R2 ]CHECK SAVED CONTENTS OF SR0
888 004720 001401 BEQ ,+4
889 004722 104006 HLT ]SR0 INCORRECT-SHOULD HAVE
890 ]TRACKED REFERENCE TO DATA SPACE,
891 ]PAGE 0, WHICH GOT THE ADDRESS
892 ]OF SR0 TO TURN OFF MEMORY MANAG.
893 004724 022777 004724 175070 CMP #,,#SR2 ]CHECK SR2
894 004732 001401 BEQ ,+4
895 004734 104006 HLT ]SR2 INCORRECT-SHOULD TRACK EVEN
896 ]WHEN MEMORY MANAG. IS OFF
897 004736 022777 077506 175126 CMP #77506,#KPDR1 ]CHECK PDR FOR
898 004744 001401 BEQ ,+4 ]THE RW PAGE REFERENCED
899 004746 104006 HLT ]KPDR1 INCORRECT-"W" BIT SHOULD
900 ]BE SET SINCE PAGE WAS WRITTEN
901 004750 022767 125252 175170 CMP #125252,DESTAD ]MAKE SURE THAT THE WRITE ACTUALLY OCCURRED
902 004756 001401 BEQ ,+4
903 004760 104006 HLT ]DATO TO RW PAGE FAILED TO WRITE CORRECT LOCATION
904 004762 000404 BR DONE33
905 004764 042777 000001 175024 RET33: BIC #1,#SR0 ]TURN OFF MEMORY MANAG.
906 004772 104006 HLT ]DATO TO RW PAGE CAUSED
907 ]A TRAP OR ABORT
908 004774 016777 175026 175022 DONE33: MOV KTSTA,#KTVEC ]RESTORE TRAP RETURN TO CAUSE HALT
909 005002 005077 175020 CLR #KTSTA ]ON AN UNEXPECTED TRAP
910 005006 005077 175004 CLR #SR0 ]INITIALIZE SR0
911 005012 005037 177776 CLR #PS ]INITIALIZE PROCESSOR STATUS
912
913 ]SHOW THAT A DATIP, DATO SEQUENCE TO A RW PAGE (ACF#6)
914 ]NEITHER TRAPS NOR ABORTS
915 ]SHOW THAT THE MEMORY MANAG. STATUS REGISTERS CONTINUE TO TRACK, AND THAT
916 ]THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
917 005016 104400 TEST13: SCOPE
918 005020 012706 001000 MOV #KSTACK,SP ]INITIALIZE KERNEL STACK POINTER
919 005024 005077 174766 CLR #SR0 ]INITIALIZE SR0
920 005030 004767 001546 JSR PC,ORDER ]CHECK TEST SEQUENCE
921 005034 000013 13 ]TEST NUMBER
922 005036 104006 HLT ]TEST EXECUTED OUT OF SEQUENCE
923 005040 012746 000006 MOV #6,-(SP) ]PUSH RW KEY ON THE STACK
924 005044 004767 000520 JSR X7,SETUP ]MAKE KERNEL PAGE 1 RW, BANK 0
925 ]MAKE KERNEL PAGE 7 RW, EXTERNAL
926 ]MAKE ALL OTHER PAGES RW, BANK 0
927 005050 005726 TST (SP)+ ]RESTORE STACK POINTER
928 005052 012777 005162 174744 MOV #RET35,#KTVEC ]SETUP ABORT RETURN IN CASE

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929 005060 005077 174742 CLR #KTSTA
930 005064 005067 175056 CLR DESTAD ]INITIALIZE LOCATION TO BE REFERENCED
931 005070 012704 022150 MOV #DESTAD+20002,R4 ]R4 CONTAINS VIRTUAL ADDRESS+2 OF
932 ]LOCATION TO BE REFERENCED THRU KERNEL PAGE 1
933 005074 005277 174716 INC #SR0 ]TURN ON MEMORY MANAG.
934 005100 005244 INC -(R4) ]DATIP, DATO TO RW PAGE-SHOULDN'T TRAP OR ABORT
935 005102 017702 174710 MOV #SR0,R2 ]SAVE CONTENTS OF SR0
936 005106 105077 174704 CLR #SR0 ]TURN OFF MEMORY MANAG.
937 005112 022702 000017 CMP #17,R2 ]CHECK SAVED CONTENTS OF SR0
938 005116 001401 BEQ ,+4
939 005120 104006 HLT ]SR0 INCORRECT-SHOULD HAVE
940 ]TRACKED REFERENCE TO DATA SPACE,
941 ]PAGE 0, WHICH GOT THE ADDRESS
942 ]OF SR0 TO TURN OFF MEMORY MANAG.
943 005122 022777 005122 174672 CMP #,,#SR2 ]CHECK SR2
944 005130 001401 BEQ ,+4
945 005132 104006 HLT ]SR2 INCORRECT-SHOULD TRACK EVEN
946 ]WHEN MEMORY MANAG. IS OFF
947 005134 022777 077506 174730 CMP #77506,#KPDR1 ]CHECK PDR CORRESPONDING
948 005142 001401 BEQ ,+4 ]TO THE RW REFERENCE
949 005144 104006 HLT ]KPDR1 INCORRECT - "W" BIT SHOULD BE SET
950 005146 022767 000001 174772 CMP #1,DESTAD ]MAKE CERTAIN THAT THE INSTRUCTION WAS EXECUTED
951 005154 001401 BEQ ,+4
952 005156 104006 HLT ]DATIP, DATO TO RW PAGE DIDN'T EXECUTE CORRECTLY
953 005160 000404 BR DONE35
954 005162 042777 000001 174626 RET35: BIC #1,#SR0 ]TURN OFF MEMORY MANAG.
955 005170 104006 HLT ]DATIP, DATO TO RW PAGE CAUSED
956 ]A TRAP OR ABORT
957 005172 016777 174630 174624 DONE35: MOV KTSTA,#KTVEC ]RESTORE TRAP RETURN TO CAUSE HALT
958 005200 005077 174622 CLR #KTSTA ]ON AN UNEXPECTED TRAP
959 005204 005077 174606 CLR #SR0 ]INITIALIZE SR0
960 005210 005037 177776 CLR #PS ]INITIALIZE PROCESSOR STATUS
961
962 ]SHOW THAT A DATIP, DATOB SEQUENCE TO A RW PAGE (ACF#6)
963 ]NEITHER TRAPS NOR ABORTS
964 ]SHOW THAT THE MEMORY MANAG. STATUS REGISTERS CONTINUE TO TRACK, AND THAT
965 ]THE PDR CORRESPONDING TO THE REFERENCE IS CORRECT
966 TEST14: SCOPE
967 005214 104400
968 005216 012706 001000 MOV #KSTACK,SP ]INITIALIZE KERNEL STACK POINTER
969 005222 005077 174570 CLR #SR0 ]INITIALIZE SR0
970 005226 004767 001350 JSR PC,ORDER ]CHECK TEST SEQUENCE
971 005232 000014 14 ]TEST NUMBER
972 005234 104006 HLT ]TEST EXECUTED OUT OF SEQUENCE
973 005236 012746 000006 MOV #6,-(SP) ]PUSH RW KEY ON THE STACK
974 005242 004767 000322 JSR X7,SETUP ]MAKE KERNEL PAGE 1 RW, BANK 0
975 ]MAKE KERNEL PAGE 7 RW, EXTERNAL
976 ]MAKE ALL OTHER PAGES RW, BANK 0
977 005246 005726 TST (SP)+ ]RESTORE STACK POINTER
978 005250 012777 005360 174546 MOV #RET37,#KTVEC ]SETUP ABORT RETURN IN CASE
979 005256 005077 174544 CLR #KTSTA
980 005262 005067 174660 CLR DESTAD ]INITIALIZE LOCATION TO BE REFERENCED
981 005266 012703 022147 MOV #DESTAD+20001,R3 ]R3 CONTAINS VIRTUAL ADDRESS+1 OF
982 ]LOCATION TO BE REFERENCED THRU KERNEL PAGE 1

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983 005272 005277 174520      INC    #SR0      ;TURN ON MEMORY MANAG.
984 005276 105343      DECB   -(R3)    ;DATIP, DATOB TO RW PAGE-SHOULDN'T TRAP OR ABORT
985 005300 017702 174512      MOV    #SR0,R2 ;SAVE CONTENTS OF SR0
986 005304 105377 174506      DECB   #SR0    ;TURN OFF MEMORY MANAG.
987 005310 022702 000017      CMP    #17,R2  ;CHECK SAVED CONTENTS OF SR0
988 005314 001401      BEQ    ,+4
989 005316 104006      HLT
990
991
992
993 005320 022777 005320 174474      CMP    #,,#SR2 ;SR0 INCORRECT-SHOULD HAVE
994 005326 001401      BEQ    ,+4    ;TRACKED REFERENCE TO DATA SPACE,
995 005330 104006      HLT          ;PAGE 0, WHICH GOT THE ADDRESS
996
997 005332 022777 077506 174532      CMP    #77506,#KPDRI ;SR2 INCORRECT-SHOULD TRACK EVEN
998 005340 001401      BEQ    ,+4    ;WHEN MEMORY MANAG. IS OFF
999 005342 104006      HLT          ;CHECK PDR CORRESPONDING
1000 005344 022767 000377 174574      CMP    #377,DESTAD ;TO THE RW REFERENCE
1001 005352 001401      BEQ    ,+4    ;KPDRI INCORRECT - "W" BIT SHOULD BE SET
1002 005354 104006      HLT          ;MAKE CERTAIN THAT THE INSTRUCTION WAS EXECUTED
1003 005356 000404      BR     DONE37 ;DATIP, DATOB TO RW PAGE DIDN'T EXECUTE CORRECTLY
1004 005360 042777 000001 174430 RET37: BIC    #1,#SR0 ;TURN OFF MEMORY MANAG.
1005 005366 104006      HLT          ;DATIP, DATOB TO RW PAGE CAUSED
1006
1007 005370 016777 174432 174426 DONE37: MOV   KTSTA,#KTVEC ;RESTORE TRAP RETURN TO CAUSE HALT
1008 005376 005077 174424      CLR   #KTSTA  ;ON AN UNEXPECTED TRAP
1009 005402 005077 174410      CLR   #SR0    ;INITIALIZE SR0
1010 005406 005037 177776      CLR   ##PS    ;INITIALIZE PROCESSOR STATUS
1011
1012 005412 104400      SCOPE
1013
1014 005414 004767 001032      JSR   X7,BELL
1015
1016 005420 013701 000042      MOV   #042,R1 ;MONITOR HOOK
1017 005424 001405      BEQ   END
1018 005426 000005      RESET
1019 005430 004711      LOGIC: JSR   X7,#R1
1020 005432 000240      NOP
1021 005434 000240      NOP
1022 005436 000240      NOP
1023 005440 000167 174506      END:  JMP   START
  
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1024
1025      J
1026 005444 030461 031457 020064 MTIT:  .ASCII '11/34 MEMORY MANAG. ACCESS KEYS TEST#'
1027 005452 042515 047515 054522
1028 005460 046440 047101 043501
1029 005466 020056 041501 042503
1030 005474 051523 045440 054505
1031 005502 020123 042524 052123
1032 005510      100
1033 005511      120 036503 040040 MPC:  .ASCII 'PC= #'
1034 005516 020040 051520 020075 MPS:  .ASCII ' PS= #'
1035 005524      100
1036 005526      ,EVEN
1037      ;SUBROUTINE TO MAKE ALL PAGES RW, BANK 0, 4K, UP
1038 005526 005077 174264      RWALL: CLR   #SR0
1039 005532 012701 002030      MOV   #ADRTAB,R1
1040 005536 012700 000010      RWL1: MOV   #10,R0
1041 005542 005071 000020      RWL2: CLR   #20(R1)
1042 005546 012731 077406      MOV   #77406,#(R1)+
1043 005552 077005      SOB   R0,RWL2
1044 005554 062701 000020      ADD   #20,R1
1045 005560 020127 002126      CMP   R1,#ADREND
1046 005564 003764      BLE   RWL1
1047 005566 000207      RTS   X7
1048
1049      ;SUBROUTINE TO SET ALL PAGES RW EXCEPT KERNEL PAGE 1
1050 ;KERNEL PAGE 1 IS SET TO DESIRED KEY
1051 ;KEY IS PASSED VIA THE STACK
1052 ;ALL PAGES ARE MAPPED TO BANK 0 EXCEPT KERNEL PAGE 7, WHICH IS MAPPED TO
1053 ;THE EXTERNAL BANK
1054 005570 004767 177732      SETUP: JSR   X7,RWALL ;INITIALLY MAP ALL PAGES RW, BANK 0
1055 005574 012777 077400 174270      MOV   #77400,#KPDRI ;MAKE KERNEL PAGE ONE 4K, UP
1056 005602 056677 000002 174262      BIS   2(SR),#KPDRI ;SET TO DESIRED KEY
1057 005610 012777 007600 174310      MOV   #7600,#KPART ;MAP KERNEL PAGE 7 EXTERNAL
1058 005616 000207      RTS   X7
1059
1060 ;ROUTINE TO LOOP THRU A SINGLE INSTRUCTION TEST
1061 ;LOAD THE STARTING ADDRESS OF THE TEST
1062 ;YOU WISH TO RUN (THE ADDRESS OF THE TEST)
1063 ;TAG IN THE LOCATION "RETRNX",, SET SWITCH REGISTER
1064 ;OPTIONS .
1065 ;NOTE THAT SW11 MUST BE DOWN TO RUN THIS TEST
1065 005620 005037 177776      TESTX: CLR   ##PS
1066 005624 012706 001000      MOV   #KSTACK,SP
1067 005630 012737 140000 177776      MOV   #140000,#PS ;SETUP USER STACK POINTER
1068 005636 012706 002000      MOV   #USTACK,SP
1069 005642 005037 177776      CLR   ##PS
1070 005646 062767 000002 000030      ADD   #2,RETRNX ;ADD 2 TO POINT TO INSTRUCTION AFTER
1071 005654 000000      HALT ;SET SR OPTIONS
1072 005656 005067 000120      CLR   SCOPEF ;KEEP COUNT AT ZERO
1073 005662 012767 000114      MOV   #05674 ;LOAD SCOPE LOOP RETURN POINTER
1074 005670 000177 000010      JMP   #RETRNX ;JUMP TO TEST
1075 005674 005067 000102      XLOOP: CLR   SCOPEF ;KEEP COUNT AT ZERO
1076 005700 000177 000000      JMP   #RETRNX ;JUMP TO TEST
1077 005724 000000      RETRNX: 0
  
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1078
1079
1080 005706 032777 040000 174234 ]SCOPE AND/OR ITERATION LOOP FOR EACH TEST 4000 TIMES
SCOPE: BIT #4000,#SR ]TEST SR FOR SCOPE
1081 005714 001015 ]ONE SCOPE ]YES,SCOPE
1082 005716 032777 004000 174224 ]BIT #4000,#SR ]NO-TEST FOR ITERATION
1083 005724 001016 ]ONE SCOPE ]INHIBIT ITERATION
1084 005726 026767 000050 000044 ]CMP SCOPE, ICOUNT ]COMPARE CURRENT COUNT TO MAX NUMBER
1085 005734 100012 ]BPL SCOPE ]EXIT-DONE
1086 005736 005267 000040 ]INC SCOPE ]INCREMENT COUNT
1087 005742 012737 000340 177776 ]MOV #340,##PS ]PREVENT TRAPPING WHILE MOVING STACK
1088 005750 022606 ]CMP (6)+,X6 ]REPOSITION STACK
1089 005752 012637 177776 ]MOV (6)+,##PS ]RESTORE PREVIOUS PROCESSOR STATUS
1090 005756 000177 000022 ]JMP ]REPEAT TEST
1091 005762 005067 000014 ]SCOPE: CLR SCOPE ]CLEAR COUNT
1092 005766 005267 000674 ]INC TESTCT ]STEP TEST COUNTER
1093 005772 011667 000006 ]MOV #X6,RETURN ]SAVE SCOPE RETURN POINTER
1094 005776 000002 ]RTI ]RETURN INLINE-NEXT TEST
1095 006000 004000 ]ICOUNT: 4000 ]ITERATION COUNT
1096 006002 000000 ]SCOPE: 0 ]COUNT LOCATION FOR ITERATION LOOP
1097 006004 000000 ]RETURN: 0 ]ADDRESS OF LAST TEST
1098
1099
1100 ]ENTERED WITH SYSTEM TRAP CALL (HLT)
1101 ]PRINT OUT THE ERROR PC+2 AND STATUS REGISTER
1102 006006 012767 000340 171762 ]PRINT: MOV #340,PS ]SET PRIORITY TO 7
1103 006014 037727 174130 020000 ]BIT #SR,#20000 ]TEST FOR INHIBIT PRINT OUT
1104 006022 001401 ]BEQ ]BRANCH TO PRINT
1105 006024 000432 ]BR ]INHIBIT, CHECK FOR HALT
1106 006026 012667 000072 ]MOV (6)+,SAVPC ]PC OF FAILING ROUTINE
1107 006032 012667 000070 ]MOV (6)+,SAVPSR ]PSR OF ERROR CONDITION
1108 006036 024646 ]CMP -(6),-(6) ]RESTORE STACK
1109 006040 012767 000200 171730 ]MOV #200,PS
1110 006046 004767 000416 ]JSR ]OUTPUT CARRIAGE RETURN AND LINE FEED
1111 006052 016767 000046 000314 ]MOV SAVPC,PTEMP1 ]LOAD WITH FAILING PC+2
1112 006060 004767 000436 ]JSR PC,TYPE
1113 006064 005511 ]MPC
1114 006066 004767 000036 ]JSR PC,PRSHRT
1115 006072 004767 000424 ]JSR PC,TYPE
1116 006076 005516 ]MPS
1117 006100 016767 000022 000266 ]MOV SAVPSR,PTEMP1 ]LOAD PROCESSOR STATUS
1118 006106 004767 000050 ]JSR ]PRINT PROCESSOR STATUS
1119 006112 005777 174032 ]CK: TST #SR ]CHECK SR FOR HALT SWITCH
1120 006116 100001 ]BPL ]BRANCH IF NOT SET
1121 006120 000000 ]HALT ]HALT ON ERROR UP
1122 006122 000002 ]RTI ]RETURN TO MAIN LINE
1123 006124 000000 ]SAVPC: 0
1124 006126 000000 ]SAVPSR: 0

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1125
1126 ]SUBROUTINE TO PRINT OUT OCTAL NUMBER
1127 ]PRSHRT DELETES LEADING ZEROS
1128 ]PROCT PRINTS OUT 6 OCTAL DIGITS
1129 006130 012767 000001 000232 ]PRSHRT: MOV #1,PRSFGL ]SET FLAG TO INDICATE SHORT PRINTOUT
1130 006136 005767 000232 ]TST PTEMP1 ]CHECK FOR ZERO
1131 006142 001011 ]BNE ]BRANCH IF NOT ZERO
1132 006144 012777 000260 173642 ]MOV #260,#TOBR ]OUTPUT A SINGLE ZERO
1133 006152 105777 173634 ]TSTB #TOCSR ]WAIT FOR TTY READY
1134 006156 100375 ]BPL ]BRANCH IF NOT SET
1135 006160 000207 ]RTS ]RETURN
1136 006162 005067 000202 ]PROCT: CLR PRSFGL ]CLEAR FLAG TO INDICATE FULL PRINTOUT
1137 006166 005067 000206 ]CLR PTEMP3 ]CLEAR R4 FOR COUNTING CHARACTERS OUTPUT
1138 006172 005067 000174 ]CLR PRFLG ]INITIALIZE CARRY FLAG FOR ROTATES
1139 006176 012767 000260 000172 ]MOV #260,PTEMP2 ]SETUP R3
1140 006204 005767 000164 ]TST PTEMP1 ]CHECK BIT 15 OF NUMBER
1141 006210 100002 ]BPL ]BRANCH IF ZERO
1142 006212 005267 000160 ]INC PTEMP2 ]INCREMENT R3 IF ONE
1143 006216 006167 000152 ]ROL PTEMP1 ]ROTATE LEFT MOST OCTAL TO RIGHT END
1144 006222 006167 000146 ]ROL PTEMP1
1145 006226 005567 000140 ]ADC PRFLG ]STORE CARRY
1146 006232 005767 000132 ]P,CK: TST PRSFGL ]CHECK FOR SHORT PRINTOUT
1147 006236 021404 ]BEQ P,WAIT ]BRANCH IF NOT SET
1148 006240 026727 000132 000260 ]CMP PTEMP2,#260 ]CHECK FOR ZERO IF SET
1149 006246 001410 ]BEQ P,CONT ]IF SET, GO TO NEXT CHARACTER
1150 006250 016777 173536 ]P,WAIT: MOV PTEMP2,#TOBR ]OUTPUT NEXT CHARACTER
1151 006256 105777 173530 ]TSTB #TOCSR ]WAIT FOR TTY READY
1152 006262 100375 ]BPL ]BRANCH IF NOT SET
1153 006264 005067 000100 ]CLR PRSFGL ]PRINT REST OF NUMBER AFTER A NON-ZERO DIGIT
1154 006270 005267 000104 ]P,CONT: INC ]COUNT
1155 006274 026727 000100 000006 ]CMP PTEMP3,#6 ]CHECK FOR DONE
1156 006302 001001 ]BNE P,CNT1 ]BRANCH IF NOT DONE
1157 006304 000207 ]RTS
1158 006306 000241 ]P,CNT1: CLC ]CLEAR CARRY
1159 006310 005767 000056 ]TST PRFLG ]CHECK FOR PREVIOUS CARRY
1160 006314 001403 ]BEQ ]BRANCH IF PREVIOUSLY ZERO
1161 006316 005067 000050 ]CLR PRFLG ]INITIALIZE FLAG
1162 006322 000261 ]SEC ]SET CARRY
1163 006324 006167 000044 ]ROL PTEMP1 ]ROTATE NEXT CHARACTER INTO RIGHT END OF REGISTER
1164 006330 006167 000040 ]ROL PTEMP1
1165 006334 006167 000034 ]ROL PTEMP1
1166 006340 005567 000026 ]ADC PRFLG ]STORE CARRY
1167 006344 016767 000024 000024 ]MOV PTEMP1,PTEMP2 ]LOAD DATA INTO R3
1168 006352 042767 177770 000016 ]R1C #177770,PTEMP2 ]CLEAR ALL BUT LOWEST OCTAL DIGIT
1169 006360 052767 000260 000010 ]BIS #260,PTEMP2 ]SET TO ASCII EQUIVALENT
1170 006366 000721 ]BR ]LOOP
1171 006370 000000 ]PRSFGL: 0
1172 006372 000000 ]PRFLG: 0
1173 006374 000000 ]PTEMP1: 0
1174 006376 000000 ]PTEMP2: 0 ]CONTAINS VALUE TO BE OUTPUT
1175 006400 000000 ]PTEMP3: 0 ]SCRATCH ]USED TO COUNT CHARACTERS OUTPUT

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1176
1177
1178          TEMT HANDLER
1179          ;FIRST 3 CALLS LEFT OPEN IN TABLE FOR EASY PATCHES
1180 006402 011667 000032          EMTSRV: MOV  #SP,EPC          IGET CALL
1181 006406 162767 000002 000024          SUB  #2,EPC
1182 006414 017767 000020 000016          MOV  #EPC,EPC
1183 006422 105067 000013          CLR8  EPC+1          ;SAVE OFFSET ONLY
1184 006426 062767 006442 000004          ADD  #EMTAB,EPC      ;POINT TO TABLE OF ADDRESSES
1185 006434 017707 000000          MOV  #EPC,PC        ;JUMP TO DESIRED ROUTINE
1186          EPC: 0
1187          PATCH1=0          ;SUBSTITUTE 104000 WHERE 1ST PATCH IS NEEDED
1188          PATCH2=0          ;104002 FOR 2ND PATCH
1189          PATCH3=0          ;104004 FOR 3RD PATCH
1190 006442 000000          EMTAB: PATCH1        ;LOAD ADDRESS OF 1ST PATCH HERE
1191 006444 000000          PATCH2        ;LOAD ADDRESS OF 2ND PATCH HERE
1192 006446 000000          PATCH3        ;LOAD ADDRESS OF 3RD PATCH HERE
1193          PRINT
1194
1195          ;BELL ON PASS COMPLETE
1196 006452 012777 000207 173334          BELL:  MOV  #207,#TDBR
1197 006460 105777 173326          TSTB #TCSR
1198 006464 100375          BPL  ,-4
1199 006466 000207          RTS  X7
1200
1201          ;SUBROUTINE TO OUTPUT CARRIAGE RETURN AND LINEFEED
1202 006470 012777 000215 173316          CRLF:  MOV  #215,#TDBR      ;ROUTINE CARRIAGE RETURN
1203 006476 105777 173310          TSTB #TCSR          ;WAIT FOR TTY READY
1204 006502 100375          BPL  ,-4
1205 006504 012777 000212 173302          MOV  #212,#TDBR      ;OUTPUT LINEFEED
1206 006512 105777 173274          TSTB #TCSR          ;WAIT FOR TTY READY
1207 006516 100375          BPL  ,-4
1208 006520 000207          RTS  X7          ;RETURN
  
```

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1209
1210          ;SUBROUTINE TO OUTPUT ASCII MESSAGE ON TELETYPE
1211 006522 010067 000052          TYPE:  MOV  X0,SAVR0
1212 006526 011600          MOV  (6),X0          ;GET ADDRESS THAT CONTAINS MESSAGE ADDRESS
1213 006530 062716 000002          ADD  #2,#X6          ;SET UP EXIT
1214 006534 011000          MOV  #X0,X0
1215 006536 112067 000034          TYPA:  MOV8  (0)+,TYPDAT        ;GET CHARACTER
1216 006542 122767 000100 000026          CMP8  #100,TYPDAT      ;CHECK FOR "0" CHARACTER
1217 006550 001003          BNE  TYPB          ;BRANCH IF NOT "0"
1218 006552 016700 000022          MOV  SAVR0,X0        ;RESTORE R0
1219 006556 000207          RTS  PC          ;TERMINATOR CHAR, EXIT
1220 006560 116777 000012 173226          TYPB:  MOV8  TYPDAT,#TDBR      ;OUTPUT CHAR TO PRINTER
1221 006566 105777 173220          TSTB #TCSR          ;WAIT FOR TTY READY
1222 006572 100375          BPL  ,-4
1223 006574 000760          BR  TYPA
1224 006576 000000          TYPDAT: 0
1225 006600 000000          SAVR0: 0
1226
1227          ;SUBROUTINE TO CHECK TEST SEQUENCE
1228 006602 005037 177776          ORDER: CLR  #SPS          ;CLEAR PROCESSOR STATUS
1229 006606 011667 000052          MOV  (SP),TEMPN      ;GET TEST NUMBER ADDRESS
1230 006612 017767 000046 000044          MOV  #TEMPN,TEMPN    ;GET TEST NUMBER
1231 006620 032777 002000 173522          BIT  #2000,#SR
1232 006626 001404          BEQ  ORDERB
1233 006630 016700 000030          MOV  TEMPN,R0
1234 006634 000005          RESET
1235 006636 000000          HALT
1236 006640 026767 000022 000016          ORDERB: CMP  TESTCT,TEMPN        ;IS TEST SEQUENCE CORRECT
1237 006646 001403          BEQ  ORDERA          ;YES, CONTINUE
1238 006650 062716 000002          ADD  #2,(SP)          ;UPDATE FOR ERROR RETURN
1239 006654 000207          RTS  PC
1240 006656 062716 000004          ORDERA: ADD  #4,(SP)          ;UPDATE FOR GOOD RETURN
1241 006662 000207          RTS  PC
1242 006664 000000          TEMPN: 0
1243 006666 000000          TESTCT: 0
1244          ,END
  
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ADREND	002126	ADRTAB	002030	AD21	003414	AD23	003606
AD25	004206	AD27	004206	AD4	002612	AD5	003010
AD6	003210	BELL	006452	CK	006112	CMPOK1	002426
CRLF	006470	DESTAD	002146	DONE1	002504	DDNE21	003506
DONE23	003706	DONE25	004106	DONE27	004302	DONE31	004574
DONE33	004774	DONE35	005172	DONE37	005370	DONE4	002702
DONE5	003102	DONE6	003276	EMTAB	006442	EMTSRV	006402
END	005440	EPC	006440	FTITLE	002130	HLT	104006
ICOUNT	006000	KPAR0	002110	KPAR1	002112	KPAR2	002114
KPAR3	002116	KPAR4	002120	KPAR5	002122	KPAR6	002124
KPAR7	002126	KPDR0	002070	KPDR1	002072	KPDR2	002074
KPDR3	002076	KPDR4	002100	KPDR5	002102	KPDR6	002104
KPDR7	002106	KSTACK	001000	KTSTA	002026	KTVEC	002024
LOGIC	005430	MPC	005511	MPS	005516	MTIT	005444
NOP	000240	NRCNT	002140	NRKEYS	002142	NXTST	004376
NXTST1	004414	OK31	004516	ORDER	006602	ORDERA	006656
ORDERB	006640	PATCH1	000000	PATCH2	000000	PATCH3	000000
PC	1000007	PRFLG	006372	PRINT	006006	PROCT	006162
PRSFLE	006370	PRSHRT	006130	PS	177776	PTEMP1	006374
PTEMP2	006376	PTEMP3	006400	P,CK	006232	P,CNT1	006306
P,COMT	006270	P,WAIT	006250	RERUNA	003346	RETRNX	005704
RETURN	006004	RET1	002474	RET21	003426	RET23	003624
RET25	004022	RET27	004220	RET31	004564	RET33	004764
RET35	005162	RET37	005360	RET4	002626	RETS	003024
RET6	003222	RWALL	005526	RWL1	005536	RWL2	005542
R0	1000000	R1	1000001	R2	1000002	R3	1000003
R4	1000004	R5	1000005	R6	1000006	R7	1000007
SAVPC	006124	SAVPSR	006126	SAVR0	006600	SCOPE	104400
SCOPEB	005750	SCOPEC	005706	SCOPEF	006002	SCOPEG	005762
SETUP	005570	SP	1000006	SR	002150	SR0	002016
SR0H	002132	SR1	002020	SR1H	002134	SR2	002022
SR2H	002136	START	002152	STATUS	177776	SWREG	000176
TCSR	002012	TDBR	002014	TEMPN	006664	TESTCT	006666
TESTN	000015	TESTX	005620	TEST1	002324	TEST10	004130
TEST11	004414	TEST12	004616	TEST13	005016	TEST14	005214
TEST2	002526	TEST3	002724	TEST4	003124	TEST5	003320
TEST6	003530	TEST7	003730	TYPA	006536	TYPB	006560
TYPDAT	006576	TYPE	006522	UPAR0	002050	UPAR1	002052
UPAR2	002054	UPAR3	002056	UPAR4	002060	UPAR5	002062
UPAR6	002064	UPAR7	002066	UPDR0	002030	UPDR1	002032
UPDR2	002034	UPDR3	002036	UPDR4	002040	UPDR5	002042
UPDR6	002044	UPDR7	002046	USTACK	002000	XLOOP	005674
.	006670						

ERRORS DETECTED: 0

*DFKTBA,DFKTBA,DFKTBA_SRC/SOL
 RUN=TIME: 4 8 0 SECONDS
 CORE USED: 5K